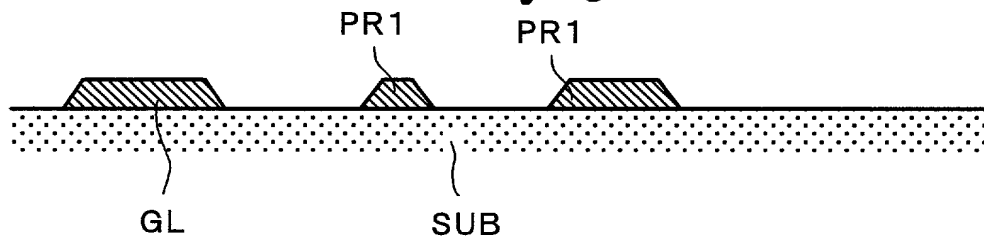
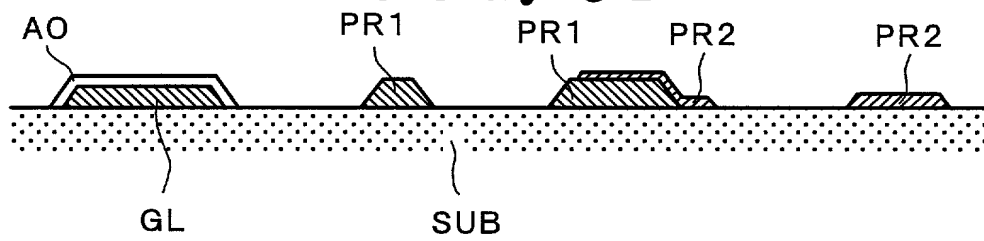
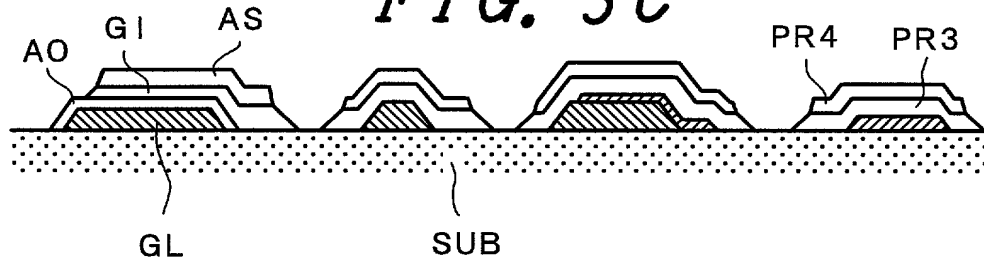
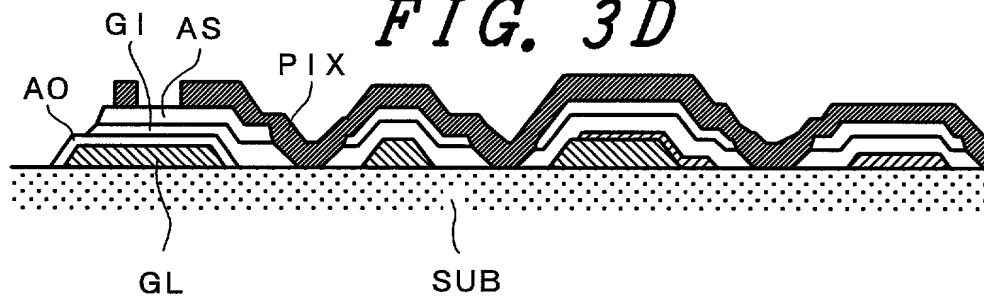
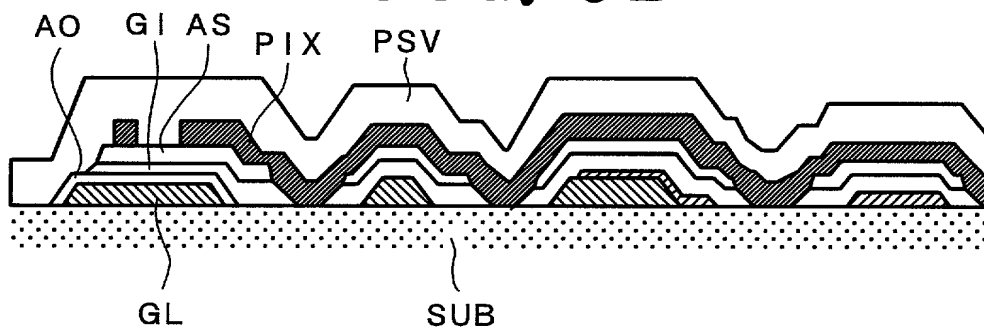


This diagram shows a plan view of a pixel array. A central rectangular area is defined by a dashed line and contains a 5x3 grid of circular pixel elements. Each pixel element consists of a central circle surrounded by concentric dashed circles. Labels include: 'PR' pointing to the pixel elements; 'Cadd' pointing to a central area; 'GL' pointing to the glass substrate; 'BM' pointing to the black matrix; 'PIX' pointing to the pixel array; 'PR1' pointing to a specific pixel; 'SD1' and 'SD2' pointing to the source and drain electrodes; 'TFT' pointing to the thin-film transistor; 'DL' pointing to the data line; 'I' pointing to the gate line; and 'PR2' pointing to another pixel. A coordinate system with 'x' and 'y' axes is shown at the bottom left.

FIG. 3A**FIG. 3B****FIG. 3C****FIG. 3D****FIG. 3E**

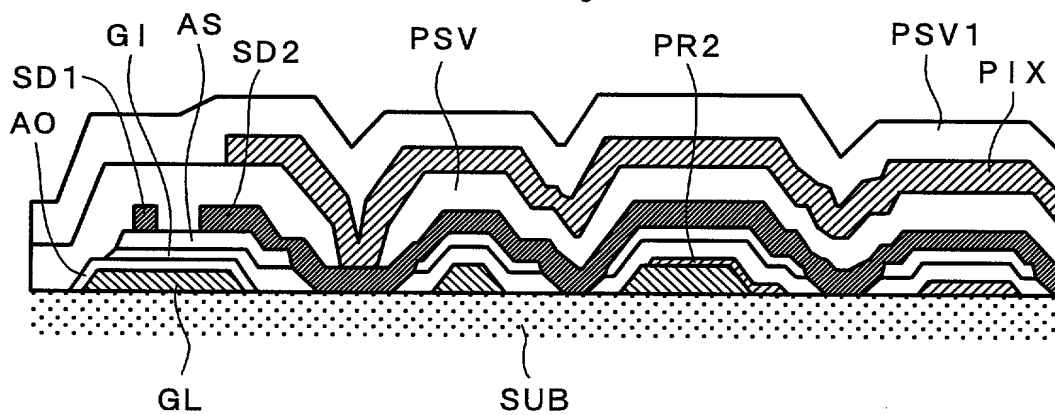


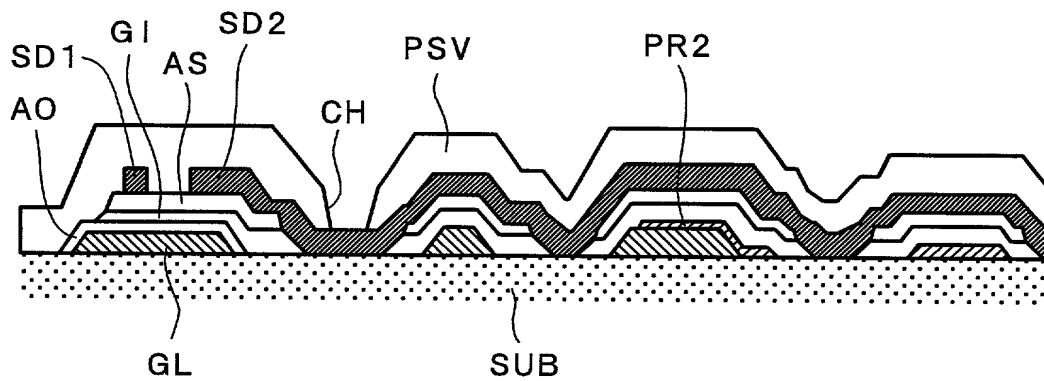
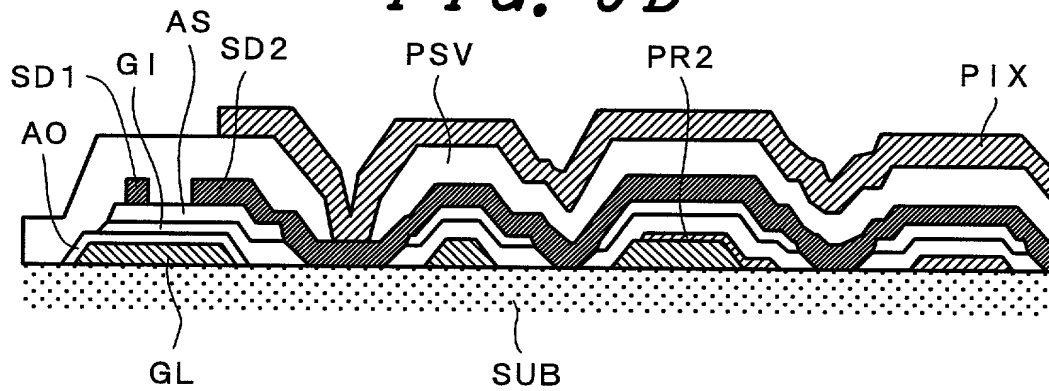
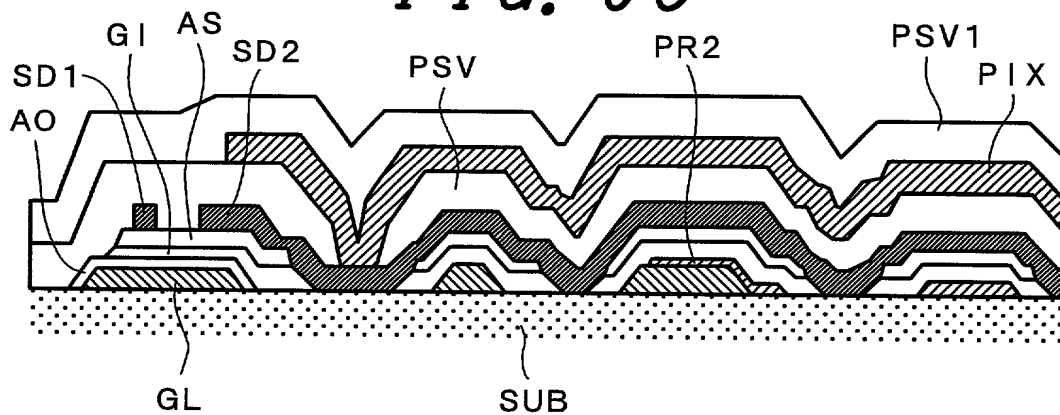
FIG. 6A**FIG. 6B****FIG. 6C**

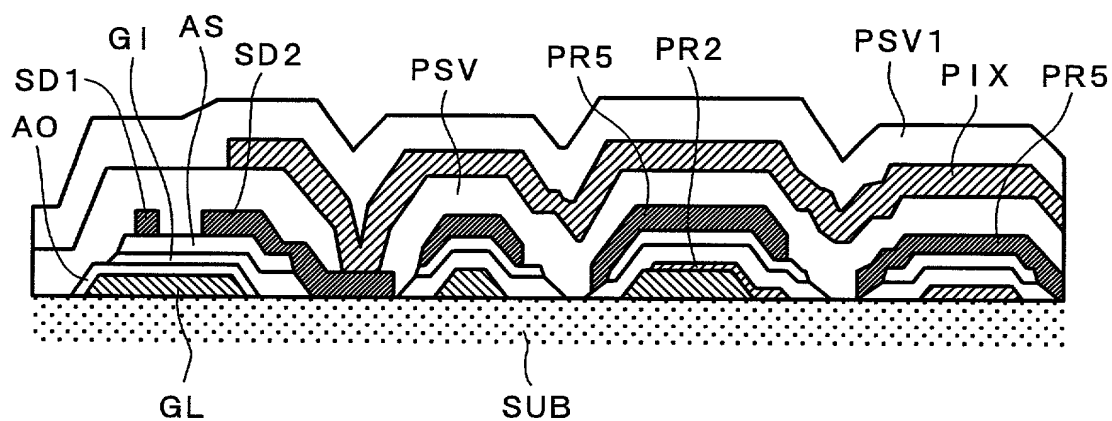
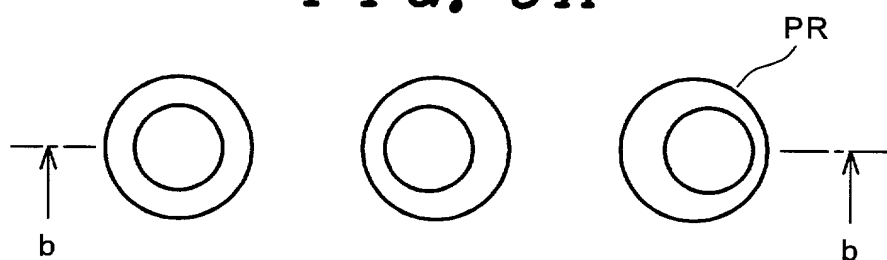
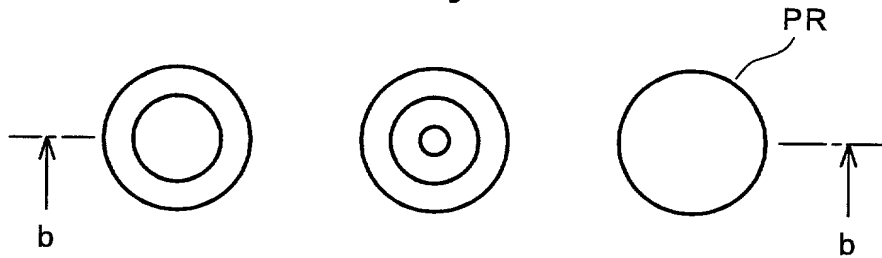
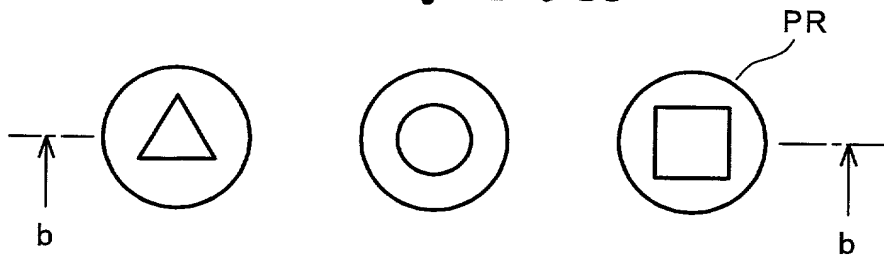
FIG. 7**FIG. 8A****FIG. 8B**

FIG. 9A*FIG. 9B**FIG. 10A**FIG. 10B*